## REMARKS

With respect to the rejection of claim 16 under Section 112, it is noted that the material in the Manual of Patent Examining Procedure, which serves as the basis of the rejection, relates solely and exclusively to means plus function claims. This is explicitly stated in the cited case.

Here, there is no means plus function claim but, instead, a specific set of structure is defined, namely, a plurality of exclusive OR gates.

Therefore, reconsideration is requested.

With respect to the rejection based on Mitra, this rejection should be reconsidered under Section 103(c). The present application and the cited application were, at the time the invention of the present application was made, owned by Intel Corporation.

In view of these remarks, the application should now be in condition for allowance.

Respectfully submitted,

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